

**GUJARAT TECHNOLOGICAL UNIVERSITY**  
**BE- SEMESTER-IV (NEW) EXAMINATION – WINTER 2020**

**Subject Code:3141002****Date:09/02/2021****Subject Name:Analog Circuit Design****Time:02:30 PM TO 04:30 PM****Total Marks:56****Instructions:**

1. Attempt any FOUR questions out of EIGHT questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.

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|------------|--|-----------|
| <b>Q.1</b> | (a) Explain Barkhausen criterion for oscillation.  | <b>03</b> |
|            | (b) Explain internal block diagram of OP-AMP   | <b>04</b> |
|            | (c) Explain the basic differentiator using an op-amp. What are the problems associated with this configuration? How they are overcome? | <b>07</b> |
| <b>Q.2</b> | (a) Explain operation of PLL with basic blocks and mention any four applications of it in radio communication.                         | <b>03</b> |
|            | (b) Explain the working of a Square wave generator using op-amp  | <b>04</b> |
|            | (c) Explain working of 555 timer based A-stable multivibrator.   | <b>07</b> |
| <b>Q.3</b> | (a) Explain the concept of V to I converter with floating load.  | <b>03</b> |
|            | (b) Explain inverting and non inverting amplifier using op-amp   | <b>04</b> |
|            | (c) Analyze second order butterworth low Pass filter. Draw its frequency response and state design procedure.                          | <b>07</b> |
| <b>Q.4</b> | (a) Explain the concept of virtual ground in op-amp.   | <b>03</b> |
|            | (b) Explain absolute wave circuit.   | <b>04</b> |
|            | (c) What is need of clipper circuit? Explain op-amp as a positive and negative clipper along with necessary waveforms.                 | <b>07</b> |
| <b>Q.5</b> | (a) What is Voltage limiter circuit?   | <b>03</b> |
|            | (b) Explain summing, scaling and averaging amplifier.  | <b>04</b> |
|            | (c) Draw op-amp based wein bridge oscillator. Obtain frequency of oscillation and discuss amplitude stabilization for same.            | <b>07</b> |
| <b>Q.6</b> | (a) Explain peak detector circuit  | <b>03</b> |
|            | (b) Explain differential amplifier using two op-amps   | <b>04</b> |
|            | (c) Draw the hybrid II common emitter transistor model. Also derive the expression for transistor trans-conductance.                   | <b>07</b> |
| <b>Q.7</b> | (a) Discuss fixed and adjustable voltage regulator with necessary circuit diagrams.  | <b>03</b> |
|            | (b) Explain the magnitude and phase response of low pass function of biquad circuit.   | <b>04</b> |
|            | (c) Derive the high frequency trans conductance equation for $g_m$ for CE amplifier.   | <b>07</b> |
| <b>Q.8</b> | (a) State the relation between hybrid-II and h-parameters.   | <b>03</b> |
|            | (b) Design RC phase oscillator for the frequency of 2 KHz.   | <b>04</b> |
|            | (c) Derive high frequency current gain for CE amplifier with $R_S$ .   | <b>07</b> |

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