

**GUJARAT TECHNOLOGICAL UNIVERSITY****BE - SEMESTER-VII (NEW) EXAMINATION – WINTER 2018****Subject Code: 2171005****Date: 19/11/2018****Subject Name: Embedded Systems****Time: 10:30 AM TO 01:00 PM****Total Marks: 70****Instructions:**

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.

		<b>MARKS</b>
<b>Q.1</b>	(a) List out the advantages of ASIC based Embedded Systems.	<b>03</b>
	(b) In absence of Digital to Analog Converter (DAC), how can you use Pulse Width Modulator for DAC operation?	<b>04</b>
	(c) Explain different approaches for designing and implementing the embedded software.	<b>07</b>
<b>Q.2</b>	(a) How Watchdog timer is used in Embedded System design?	<b>03</b>
	(b) Explain the significant differences between Bluetooth and Zigbee protocol.	<b>04</b>
	(c) Explain the differences between programmed I/O, interrupt and direct memory access approaches.	<b>07</b>
<b>OR</b>		
	(c) What is Device driver? Explain the role of Interrupt in Device driver programming.	<b>07</b>
<b>Q.3</b>	(a) Define Interrupt Service Thread with its use in RTOS based system design.	<b>03</b>
	(b) How semaphore helps in handling shared data problems?	<b>04</b>
	(c) Explain the role of TCB in task switching.	<b>07</b>
<b>OR</b>		
<b>Q.3</b>	(a) What will happen if all the tasks are in Wait state? How RTOS will handle such situation?	<b>03</b>
	(b) Describe performance matrices in RTOS based application.	<b>04</b>
	(c) Describe the Timer functions supported in RTOS.	<b>07</b>
<b>Q.4</b>	(a) List out significant differences between a soft real time system and a hard real time system.	<b>03</b>
	(b) Describe Deadlock with an example.	<b>04</b>
	(c) Describe the mailbox functions supported in Real Time Operating System.	<b>07</b>
<b>OR</b>		
<b>Q.4</b>	(a) Under which circumstances a task is brought to Wait state?	<b>03</b>
	(b) What are the differences between a function and a task?	<b>04</b>
	(c) Explain following scheduling policies with their differences. (1) Earliest Deadline First (2) Rate Monotonic Scheduler	<b>07</b>
<b>Q.5</b>	(a) What are the significant features associated with GPIO in MSP430?	<b>03</b>
	(b) Explain the benefits of using DCO over Crystal in MSP430 based system.	<b>04</b>
	(c) Describe the Watchdog timer operation in MSP430.	<b>07</b>
<b>OR</b>		
<b>Q.5</b>	(a) How MSP430 processor is compiler friendly?	<b>03</b>
	(b) Explain Pin Multiplexing in MSP430.	<b>04</b>
	(c) Describe the Timer operation in MSP430 in association with generation of PWM wave.	<b>07</b>

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