

**GUJARAT TECHNOLOGICAL UNIVERSITY**

**BE - SEMESTER-III (NEW) EXAMINATION – SUMMER 2024**

**Subject Code:3131706**

**Date:29-06-2024**

**Subject Name:Measurement and Instruments**

**Time:10:30 AM TO 01:00 PM**

**Total Marks:70**

**Instructions:**

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.
4. Simple and non-programmable scientific calculators are allowed.

	<b>Marks</b>
<b>Q.1</b> (a) Compare Accuracy & Precision.	<b>03</b>
(b) Differentiate Indicating and Recording Instruments with examples.	<b>04</b>
(c) Explain Various types of Errors in Measurement.	<b>07</b>
<b>Q.2</b> (a) Explain LCD Display.	<b>03</b>
(b) Explain Analog DC Ammeters.	<b>04</b>
(c) Explain Electromechanically meter movement with necessary diagram.	<b>07</b>
<b>OR</b>	
(c) What is Loading effect? Explain its elimination.	<b>07</b>
<b>Q.3</b> (a) Compare Oscilloscope vs Meter.	<b>03</b>
(b) Draw and Explain Cathode Ray Tube (CRT).	<b>04</b>
(c) Explain Vertical Deflection System of CRO.	<b>07</b>
<b>OR</b>	
<b>Q.3</b> (a) Explain Electronic Timers.	<b>03</b>
(b) Draw the Lissajous Pattern using CRO in XY Mode for frequency measurement of 2:1, 3:1, 3:2 and 4:1.	<b>04</b>
(c) Explain Digital Storage Oscilloscope (DSO) with its block diagram.	<b>07</b>
<b>Q.4</b> (a) Explain Power and Energy in brief.	<b>03</b>
(b) Explain Zero Beat Frequency Meter.	<b>04</b>
(c) Explain Harmonic Analysis and Spectrum Analyzers	<b>07</b>
<b>OR</b>	
<b>Q.4</b> (a) Explain Potential Transformer application.	<b>03</b>
(b) Explain Single Phase power Measurement technique with necessary diagrams.	<b>04</b>
(c) Explain Kelvin's double bridge method for low resistance Measurement.	<b>07</b>
<b>Q.5</b> (a) Explain 20 mA Current loop.	<b>03</b>
(b) Explain Maxwell Bridge with its Diagrams.	<b>04</b>
(c) Explain Inductive interference and shielding.	<b>07</b>
<b>OR</b>	
<b>Q.5</b> (a) Explain Pulse Generators	<b>03</b>
(b) Explain Anderson Bridge with its Diagrams.	<b>04</b>
(c) Explain ground loop interference and input guarding to reduce the internal noise.	<b>07</b>

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