

**GUJARAT TECHNOLOGICAL UNIVERSITY****BE - SEMESTER-VIII(NEW) EXAMINATION – SUMMER 2019****Subject Code:2180808****Date:09/05/2019****Subject Name:Embedded Systems****Time:10:30 AM TO 01:00 PM****Total Marks: 70****Instructions:**

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.

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|------------|---|-----------|
| <b>Q.1</b> | (a) Define embedded system.   | <b>03</b> |
|            | (b) Explain embedded hardware units and devices in a system.        | <b>04</b> |
|            | (c) Explain basic architecture of MSP430.                           | <b>07</b> |
| <b>Q.2</b> | (a) Compare RISC and CISC architecture.                             | <b>03</b> |
|            | (b) Explain wireless and mobile system protocol in brief.           | <b>04</b> |
|            | (c) Explain serial communication devices with protocol.             | <b>07</b> |
|            | <b>OR</b>   |           |
|            | (c) Explain watchdog timer and real time clock in embedded systems. | <b>07</b> |
| <b>Q.3</b> | (a) Explain I/O busy-wait approach in brief.                        | <b>03</b> |
|            | (b) Explain DMA (Direct Memory Access) in brief.                    | <b>04</b> |
|            | (c) Explain Interrupt Servicing (Handling) Mechanism in detail.     | <b>07</b> |
|            | <b>OR</b>   |           |
| <b>Q.3</b> | (a) Explain Interrupt Sources and ISR Concept.                      | <b>03</b> |
|            | (b) Define (i) Process (ii) Thread (iii) Task                       | <b>04</b> |
|            | (c) Explain Inter process communication in detail.                  | <b>07</b> |
| <b>Q.4</b> | (a) Explain message queue functions in brief.                       | <b>03</b> |
|            | (b) Explain Semaphores and their functions in embedded system       | <b>04</b> |
|            | (c) Explain Real Time Operating System (RTOS) in detail.            | <b>07</b> |
|            | <b>OR</b>   |           |
| <b>Q.4</b> | (a) Explain file system management in RTOS                          | <b>03</b> |
|            | (b) Explain Memory management in RTOS.                              | <b>04</b> |
|            | (c) Explain Task scheduling in RTOS.                                | <b>07</b> |
| <b>Q.5</b> | (a) Explain RTOS security issues in brief.                          | <b>03</b> |
|            | (b) Explain Process Management in RTOS.                             | <b>04</b> |
|            | (c) Explain ADC module in MSP430.                                   | <b>07</b> |
|            | <b>OR</b>   |           |
| <b>Q.5</b> | (a) Enlist on-chip peripherals in MSP430.                           | <b>03</b> |
|            | (b) Explain basic features of CCS (Code Composer Studio).           | <b>04</b> |
|            | (c) Explain I/O ports in MSP430.                                    | <b>07</b> |